



PANIMALAR ENGINEERING COLLEGE

An Autonomous Institution, Affiliated to Anna University, Chennai
A Christian Minority Institution

(JAISAKTHI EDUCATIONAL TRUST)

Approved by All India Council for Technical Education



END SEMESTER PRACTICAL EXAMINATIONS – MARCH 2024 Schedule of Practical Examinations and Nomination of Examiners

Name of the Programme & Branch: B.E Electrical and Electronics Engineering

TOTAL STRENGTH-182

SL NO	Degree & Branch	Semester	Date & Day of Exam	Time (Session)	No. of Candidates, (Batches)	Batch /Session No.	Subject Code	Name of the Practical	Name of the Internal Examiner Appointed
1	B.E Electrical and Electronics Engineering	08	02.04.2025 (Wednesday)	8.30 a.m.	27 (8)	A1-A8/1	21EE1811	PROJECT WORK	Dr.S.SELVI
2	B.E Electrical and Electronics Engineering	08	02.04.2025 (Wednesday)	12.00 Noon	27 (8)	A9-A16/2	21EE1811	PROJECT WORK	Dr.S.SELVI
3	B.E Electrical and Electronics Engineering	08	03.04.2025 (Thursday)	8.30 a.m.	34 (9)	B1-B9/3	21EE1811	PROJECT WORK	Dr.S.SELVI
4	B.E Electrical and Electronics Engineering	08	03.04.2025 (Thursday)	12.00 Noon	30 (8)	B10-B17/4 (Except 211421105314, 211421105319)	21EE1811	PROJECT WORK	Dr.S.SELVI
5	B.E Electrical and Electronics Engineering	08	04.04.2025 (Friday)	8.30 a.m.	36 (9)	C1-C9/5	21EE1811	PROJECT WORK	Dr.S.SELVI
6	B.E Electrical and Electronics Engineering	08	04.04.2025 (Friday)	12.00 Noon	28 (8)	C10-C17/6	21EE1811	PROJECT WORK	Dr.S.SELVI

- Do Not Merge any Cells in the Table

Signature of the Head of the Department

Note:

- The Number of Batches shall not exceed 10 per session. Time – Forenoon Session – 8.30 AM to 11.30 AM and Afternoon Session – 12.00 Noon to 3.00 PM

HEAD OF THE DEPARTMENT
PANIMALAR ENGINEERING COLLEGE
POONAMALLEE, CHENNAI - 600 123.